REMARKS/ARGUMENT

In the most recent Office Action, claims 1-3 were examined. Claims 1 and 2 are amended. Claims 4-12 are added. Accordingly, claims 1-12 are presently pending. No new matter is added.

Claim Rejections - 35 U.S.C. §112

Claims 2 and 3 are rejected under 35 U.S.C. §112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter that Applicant regards as the invention. In particular, the Office Action states that claim 2 contains limitations lacking antecedent basis.

Applicant has amended claim 2 to correct antecedent basis and overcome the rejection. Accordingly, Applicant respectfully requests that the rejection of claims 2 and 3 under 35 U.S.C. §112, second paragraph, be reconsidered and withdrawn.

Claim Rejections - 35 U.S.C. §102

Claim 1 is rejected under 35 U.S.C. 102(e) as being anticipated by Wilkinson (U.S. Patent No. 5,812,125). In particular, the Office Action states that Wilkinson discloses all of the elements recited in claim 1.

Applicant respectfully notes that a rejection under 35 U.S.C. 102 can be overcome if it can be shown that the rejected claim contains at least one limitation not shown in any of the cited prior art references. With regard to the disclosure by Wilkinson, Applicant respectfully notes that the cited reference does not contain a gate in a capture-only path that can control the flow of video data received from the video processor, as is recited in claim 1.

Accordingly, Applicant respectfully submits that claim 1 recites at least several elements neither disclosed nor shown by Wilkinson. Applicant respectfully submits that the rejection of claim 1 under 35 U.S.C. §102 is overcome, and requests that the rejection be reconsidered and withdrawn.

Allowable Subject Matter

Applicant respectfully acknowledges the allowability of claims 2 and 3 if rewritten to overcome any rejections in the Office Action and to include all the limitations of the base claim and any intervening claims. Accordingly, claim 2 is amended to include all of the limitations of the base claim. Claim 3 depends upon and includes all of the limitations of claim 2, and is thus thought to be

allowable for at least that reason, in addition to the further limitations contained therein. Accordingly, Applicant respectfully requests notice to the effect of allowance of claims 2 and 3.

Applicant presents for consideration new claims 4-12 as reciting subject matter already in the application that Applicant has a right to claim. Claims 4-6 depend upon and include all the subject matter of claim 1, in addition to any further limitation contained in each claim. Applicant believes that claims 4-6 are thus allowable as dependent upon an allowable base claim. New claims 7-12 are believed to further define the present invention as a method that can be practiced with a device such as that described in claim 1. Accordingly, Applicant respectfully believes that claims 7-12 recite patentable subject matter, and are therefore allowable.

Conclusion

In view of the foregoing amendments and discussion, Applicant respectfully believes that the application is now in condition for allowance, and earnestly solicits notice to that effect. Applicant submits that the foregoing is a complete and accurate response to the most recent Office Action, and respectfully requests early and favorable action upon its entry. If it is believed that an interview would contribute to allowance of the claims, the Examiner is requested to contacted the undersigned counsel at the number given below.
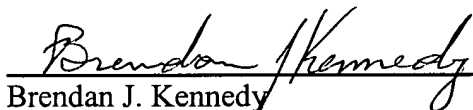
Respectfully submitted,

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VERSION WITH MARKINGS TO SHOW CHANGES MADE

37 C.F.R. § 1.121(b)(iii) AND (c)(ii)

SPECIFICATION:

<u>Replacement for the paragraph beginning at page 2, line 22</u>:

The present invention seeks to solve the problems associated with the prior art described above. It is an object of the present invention [is] to provide a video data transfer system [which] <u>that</u> increases the capturing rate of video data to be sent to the system memory.

<u>Replacement for the paragraph beginning at page 6, line 1</u>:

Then, the system checks the video data if it may be transferred to the system bus 17 (step 110). If the video data may be transferred, it is transferred to the system bus 17 and stored in the system memory 18 (step 111). If, in step 110, the system determines that the video data may not be transferred to the system bus 17 for some reason, the system checks the vertical synchronization signal data to see if the video data is the last part of a field (step 112). If the video data is not the last part of a <u>field</u> [filed], the system transfers the next data to the FIFO memory 24 (steps 108 and 109); if the video data is the last part of a field, the system closes the gate 23 to stop data transfer and suspends frame capturing (step 113).

<u>Replacement for the paragraph beginning at page 7, line 19</u>:

On the other hand, the capturing-only path 26 comprises the gate 23 <u>that</u> [which] enables/disables the FIFO memory 24, and the 32-bit-by-640-stage (YUV 16 bits, 2 lines of video data) FIFO memory 24.

CLAIMS:

1. (Amended) A video data transfer system comprising:

a real time output path through which video data processed by a video processor is sent to a display via a frame buffer; [and]

a capturing-only path which is independent of said real time output path and through which said video data is sent to a system memory via a system bus<u>; and</u>

a gate in said capturing-only path, said gate being controllable to permit said video data to pass when received from said video processor.

2. (Amended) A video data transfer system [as defined by claim 1], comprising:

a real time output path through which video data processed by a video processor is sent to a display via a frame buffer;

a capturing-only path which is independent of said real time output path and through which said video data is sent to a system memory via a system bus, wherein

said real time output path comprises:

an off-screen memory which receives video data from said video processor via a data bus and stores video data therein, said off-screen memory being in the frame buffer; and

a display control circuit which receives video data read from said off-screen memory via said data bus for enlargement and interpolation processing and transfers processed results to said display, and wherein

said capturing-only [circuit] path comprises:

a gate which is opened only when video data is received from said video processor for capturing; and

memory means for storing said video data sent through said gate and for transferring said video data to said system bus.